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| <p>(21) International Application Number: PCT/SG98/00076</p> <p>(22) International Filing Date: 25 September 1998 (25.09.98)</p> <p>(71) Applicants (for all designated States except US): STMICRO-ELECTRONICS ASIA PACIFIC PTE LTD [SG/SG]; 28 Ang Mo Kio Industrial Park 2, Singapore 569508 (SG). STMICROELECTRONICS S.A. [FR/FR]; 7, avenue Gallieni, F-94253 Gentilly Cedex (FR).</p> <p>(72) Inventors; and (75) Inventors/Applicants (for US only): DESPREZ-LE GOARANT, Yann [FR/SG]; 28 Ang Mo Kio Industrial Park 2, Singapore 569508 (SG). JAFFARD, Jean-Luc [FR/SG]; 4 bis, rue du Cotaire, F-38120 Saint Egrève (FR). MICHON, Christian [FR/FR]; 28 Ang Mo Kio Industrial Park 2, Singapore 569508 (SG).</p> <p>(74) Agent: DONALDSON & BURKINSHAW; P.O. Box 3667, Singapore 905667 (SG).</p> | | <p>(81) Designated States: JP, SG, US, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</p> <p>Published <i>With international search report.</i></p> |

(54) Title: A DIGITAL CUT-OFF CONTROL LOOP FOR TV USING SPEEDING AND BLANKING CIRCUITS

(57) Abstract

An improved digital cut-off control loop for black level adjustment in a video processor for controlling RGB output signals. The improved circuit advantageously provides a higher resolution of the black level adjustment, and a short cut-off convergence time when the TV set is switched on. The improved circuit can also blank the RGB output when the cut-off control loop has not converged to the correct level.

